

## IN THE U.S. PATENT AND TRADEMARK OFFICE

## Patent Application Transmittal Letter

COMMISSIONER FOR PATENTS  
Washington, D.C. 20231

Sir:

Transmitted herewith for filing under 37 CFR 1.53(b) is a(n): ☒ Utility ☐ Design☒ original patent application,☐ continuation-in-part application

INVENTOR(S): Zhizang Chen et al

TITLE: LDMOS And CMOS Integrated Circuit And Method Of Making

Enclosed are:

- ☒ The Declaration and Power of Attorney. ☐ signed ☒ unsigned or partially signed
- ☒ 11 sheets of drawings (one set) ☐ Associate Power of Attorney
- ☐ Form PTO-1449 ☒ Information Disclosure Statement and Form PTO-1449
- ☐ Priority document(s) ☒ (Other) Nonpublication Request (fee \$ )

CLAIMS AS FILED BY OTHER THAN A SMALL ENTITY				
(1) FOR	(2) NUMBER FILED	(3) NUMBER EXTRA	(4) RATE	(5) TOTALS
TOTAL CLAIMS	17 — 20	0	X \$18	\$ 0
INDEPENDENT CLAIMS	5 — 3	2	X \$80	\$ 160
ANY MULTIPLE DEPENDENT CLAIMS	0		\$270	\$ 0
BASIC FEE: Design (\$320.00 ); Utility (\$710.00 )				\$ 710
TOTAL FILING FEE				\$ 870
OTHER FEES				\$
TOTAL CHARGES TO DEPOSIT ACCOUNT				\$ 870

Charge \$ 870 to Deposit Account 08-2025. At any time during the pendency of this application, please charge any fees required or credit any over payment to Deposit Account 08-2025 pursuant to 37 CFR 1.25. Additionally please charge any fees to Deposit Account 08-2025 under 37 CFR 1.16, 1.17, 1.19, 1.20 and 1.21. A duplicate copy of this sheet is enclosed.

"Express Mail" label no. EL604907429US

Date of Deposit March 26 2001

I hereby certify that this is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: Commissioner for Patents, Washington, D.C. 20231.

By Timothy F. Myers

Typed Name: Timothy F. Myers

Respectfully submitted,

Zhizang Chen et al

By Timothy F. Myers

Timothy F. Myers

Attorney/Agent for Applicant(s)

Reg. No. 42,919

Date: March 26 2001

Telephone No.: (541) 715-4197

**NONPUBLICATION REQUEST  
UNDER  
35 U.S.C. 122(b)(2)(B)(i)**

First Named Inventor **Zhizang Chen**

Title **LDMOS AND CMOS INTEGRATED CIRCUIT  
AND METHOD OF MAKING**

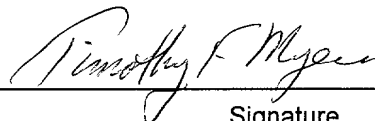
Atty Docket Number **10002861-1**

I hereby certify that the invention disclosed in the attached application **has not and will not be** the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing.

I hereby request that the attached application not be published under 35 U.S.C. 122(b).

March 26, 2001

Date

  
Signature

Timothy F. Myers, Patent Agent

Typed or printed name

US Reg. No. 42,919

This request must be signed in compliance with 37 CFR 1.33(b) and submitted with the application **upon filing**.

Applicant may rescind this nonpublication request at any time. If applicant rescinds a request that an application not be published under 35 U.S.C. 122(b), the application will be scheduled for publication at eighteen months from the earliest claimed filing date for which a benefit is claimed.

If applicant subsequently files an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications eighteen months after filing, the applicant **must** notify the United States Patent and Trademark Office of such filing within forty-five (45) days after the date of the filing of such foreign or international application. **Failure to do so will result in abandonment of this application (35 U.S.C. 122(b)(2)(B)(iii)).**